



INFORMATION DISCLOSURE CITATION

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(Use several sheets if necessary)

Docket Number (Optional) MA-584-US	Application Number 10/642,203
Applicant(s) Enomoto, et al.	
Filing Date August 18, 2003	Group Art Unit Not Yet Assigned

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Pages, Etc.*)

/BL/	Youichi HIDAKA, et al., Institute of Electronics, Information and Communication Engineers, society Meeting 2002, B-7-11 to B-7-13, "Proposal of Ethernet Architecture GOE (Global Optical Ethernet) for Future Generation", pp. 171-173.

EXAMINER /Betty Lee/ **DATE CONSIDERED** 06/22/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) MA-584-US	Application Number 10/642,203		
				Applicant(s) Nobuyuki ENOMOTO, et al.			
				Filing Date August 18, 2003	Group Art Unit 2661		
				U.S. PATENT DOCUMENTS			
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
							YES
/BL/		2002-354001	12/06/2002	Japan			ABS
/BL/		2002-353998	12/06/2002	Japan			ABS
OTHER DOCUMENTS				<i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
/BL/		Korean Office Action dated October 27, 2006 with English translation.					
EXAMINER <i>/Betty Lee/</i>				DATE CONSIDERED		06/22/2007	
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